

# architecting high performance embedded systems

**architecting high performance embedded systems** involves a meticulous design process that balances constraints such as computational power, energy efficiency, and real-time responsiveness. Embedded systems are specialized computing units integrated into larger mechanical or electrical systems, requiring tailored architectures to meet performance demands. Achieving high performance in these systems necessitates a comprehensive approach covering hardware selection, software optimization, real-time operating systems, and effective resource management. This article explores key strategies and best practices for designing embedded systems that deliver superior performance while maintaining reliability and efficiency. From hardware architecture considerations to software development methodologies, the discussion provides a detailed framework for engineers and developers. The following sections outline critical aspects of architecting high performance embedded systems, including processor choices, memory management, concurrency handling, and power optimization.

- Understanding the Fundamentals of Embedded System Architecture
- Optimizing Hardware Components for Performance
- Software Strategies to Enhance Embedded System Efficiency
- Implementing Real-Time Operating Systems (RTOS) Effectively
- Power Management and Thermal Considerations
- Testing, Debugging, and Validation Techniques

## Understanding the Fundamentals of Embedded System Architecture

Architecting high performance embedded systems begins with a solid understanding of the fundamental architecture components. Embedded systems typically include microcontrollers or microprocessors, memory units, input/output interfaces, and communication modules. The architecture must be tailored to the specific application requirements, balancing factors like processing speed, latency, and power consumption.

## Key Architectural Elements

The core of any embedded system is its processing unit, which may range from simple 8-bit microcontrollers to complex multi-core processors. Memory architecture, including RAM, ROM, and cache hierarchies, plays a crucial role in system responsiveness. Additionally, peripheral integration

and interface design impact data throughput and system scalability.

## **Architectural Trade-offs**

Designers must carefully evaluate trade-offs between performance, cost, power, and size. For example, selecting a high-speed processor may increase power consumption, while choosing low-power components could limit processing capabilities. Understanding these trade-offs is essential to architecting embedded systems that meet performance targets within given constraints.

## **Optimizing Hardware Components for Performance**

Hardware optimization is a critical aspect of architecting high performance embedded systems. The choice of components directly influences system speed, power efficiency, and reliability. Selecting appropriate processors, memory types, and communication interfaces forms the foundation of an effective embedded system.

### **Processor Selection**

Choosing the right processor involves considering factors such as instruction set architecture (ISA), clock speed, core count, and specialized features like digital signal processing (DSP) capabilities. High-performance embedded systems often benefit from multi-core or heterogeneous processors that can handle parallel tasks efficiently.

### **Memory Architecture and Management**

Memory performance affects data access speed and overall system throughput. Architecting systems with layered memory hierarchies, including caches and scratchpad memories, can reduce latency. Additionally, employing high-speed RAM and non-volatile memory technologies optimizes data storage and retrieval.

### **Peripheral and Interface Optimization**

Efficient integration of peripherals such as sensors, communication modules, and storage devices is vital. High-speed buses like SPI, I2C, and PCIe enable rapid data exchange. Optimizing these interfaces minimizes bottlenecks and enhances real-time responsiveness.

## **Software Strategies to Enhance Embedded System Efficiency**

Software plays an equally important role in architecting high performance embedded systems. Efficient coding practices, optimized algorithms, and effective resource management contribute significantly to system performance.

## **Efficient Coding and Algorithm Optimization**

Writing optimized code tailored to the hardware architecture minimizes execution time and memory usage. Algorithmic efficiency, including choosing the right data structures and minimizing computational complexity, directly impacts system responsiveness.

## **Memory and Resource Management**

Effective management of limited embedded system resources, such as RAM and processing cycles, is critical. Techniques like dynamic memory allocation, buffer management, and minimizing context switches help maintain high performance under constrained conditions.

## **Concurrency and Multithreading**

Utilizing concurrency through multithreading or multiprocessing enables parallel execution of tasks, improving throughput. Proper synchronization and avoiding race conditions ensure stability and maximize CPU utilization.

## **Implementing Real-Time Operating Systems (RTOS) Effectively**

Real-time operating systems are fundamental to many high performance embedded systems, providing deterministic task scheduling, prioritization, and resource management. Selecting and configuring an RTOS appropriately is vital for meeting strict timing requirements.

## **Choosing the Right RTOS**

Key considerations include task scheduling policies, interrupt handling, inter-process communication mechanisms, and footprint size. Lightweight RTOS options may be preferable for resource-constrained systems, whereas feature-rich RTOS platforms support complex applications.

## **Task Scheduling and Prioritization**

Efficient task scheduling ensures that critical operations meet deadlines. Preemptive scheduling and priority-based task management allow high-priority tasks to execute promptly, essential for real-time responsiveness.

## **Inter-task Communication and Synchronization**

Mechanisms like message queues, semaphores, and mutexes facilitate communication and coordination between tasks. Proper use of these tools prevents deadlocks and race conditions, maintaining system integrity and performance.

# **Power Management and Thermal Considerations**

Power efficiency is a crucial factor when architecting high performance embedded systems, especially for battery-powered or thermally constrained environments. Balancing performance with energy consumption extends system lifetime and reliability.

## **Dynamic Power Management Techniques**

Techniques such as dynamic voltage and frequency scaling (DVFS) adjust processor speed based on workload, reducing power consumption without sacrificing performance. Implementing sleep modes and power gating further conserves energy during idle periods.

## **Thermal Management Strategies**

High performance components generate heat that must be managed to prevent damage and maintain operational stability. Heat sinks, thermal throttling, and efficient system layout contribute to effective thermal dissipation.

## **Energy-Efficient Hardware Design**

Selecting low-power components and optimizing circuit design reduces overall system power requirements. Integrating energy harvesting or advanced battery technologies can also enhance sustainability for embedded applications.

## **Testing, Debugging, and Validation Techniques**

Robust testing and validation are essential in architecting high performance embedded systems to ensure functionality, reliability, and compliance with performance specifications.

## **Hardware-in-the-Loop (HIL) Testing**

HIL testing allows real-time simulation of hardware components and system interactions, enabling early detection of design flaws and performance bottlenecks before deployment.

## **Code Profiling and Performance Analysis**

Profiling tools identify computational hotspots and resource usage patterns within embedded software, guiding optimization efforts to improve execution speed and efficiency.

# **Stress Testing and Reliability Evaluation**

Subjecting systems to extreme operational conditions verifies their robustness and stability over time. This includes testing under varying temperatures, voltages, and workloads to ensure consistent high performance.

- Understand embedded system architecture fundamentals
- Optimize hardware components including processors, memory, and interfaces
- Implement efficient software practices and concurrency management
- Select and configure RTOS for deterministic real-time performance
- Apply power and thermal management techniques
- Conduct rigorous testing and validation to ensure reliability

## **Frequently Asked Questions**

### **What are the key principles of architecting high performance embedded systems?**

Key principles include optimizing hardware and software co-design, minimizing latency, efficient memory management, real-time responsiveness, power efficiency, and scalability to meet application-specific requirements.

### **How does hardware selection impact the performance of embedded systems?**

Hardware selection directly affects processing speed, power consumption, and system reliability. Choosing the right microcontroller, processor architecture, memory types, and peripherals ensures the system meets performance and efficiency goals.

### **What role does real-time operating system (RTOS) play in high performance embedded systems?**

An RTOS provides deterministic task scheduling, low interrupt latency, and resource management, which are critical for meeting timing constraints and ensuring predictable behavior in high performance embedded applications.

## **How can software optimization improve embedded system performance?**

Software optimization techniques such as efficient coding practices, loop unrolling, minimizing context switches, and using hardware accelerators can reduce execution time and resource usage, thereby enhancing overall system performance.

## **Why is power management important in high performance embedded system design?**

Power management is crucial because embedded systems often operate in constrained environments. Efficient power use extends battery life, reduces heat generation, and ensures system stability without compromising performance.

## **What design strategies help achieve low latency in embedded systems?**

Strategies include using interrupt-driven processing, prioritizing critical tasks, minimizing communication overhead, employing hardware acceleration, and optimizing memory access patterns to reduce delays.

## **How does memory architecture influence embedded system performance?**

Memory architecture affects data access speed and system responsiveness. Using appropriate types of memory (e.g., SRAM, DRAM, Flash), cache strategies, and memory hierarchy designs can significantly improve performance.

## **What is the importance of concurrency and parallelism in embedded system architecture?**

Concurrency and parallelism enable multiple tasks or processes to run simultaneously, improving throughput and responsiveness. Utilizing multi-core processors or hardware accelerators can boost performance in complex embedded systems.

## **How do communication protocols impact the performance of embedded systems?**

Efficient communication protocols reduce latency and overhead during data transfer between components or systems. Choosing protocols with low latency, high throughput, and reliability is essential for maintaining high performance.

## **Additional Resources**

1. *Embedded Systems Architecture: A Comprehensive Guide for Engineers and Programmers*

This book offers an in-depth exploration of embedded system design, focusing on the architectural

principles that drive high performance. It covers processor architectures, memory hierarchy, and real-time operating systems, providing practical insights for optimizing embedded applications. Readers will gain a solid foundation in both hardware and software aspects of embedded systems.

## *2. Designing High-Performance Embedded Systems: Hardware and Software Techniques*

Focusing on the integration of hardware and software, this book presents strategies to maximize the efficiency and speed of embedded systems. It discusses advanced topics such as low-latency communication, power management, and parallel processing. The author provides numerous case studies illustrating successful high-performance embedded designs.

## *3. Real-Time Embedded Systems: Design Principles and Engineering Practices*

This title delves into the challenges of real-time constraints in embedded systems, highlighting architectural decisions that impact system responsiveness and reliability. It includes detailed coverage of scheduling algorithms, interrupt handling, and system optimization. Engineers will find actionable techniques to architect systems that meet stringent timing requirements.

## *4. Optimizing Embedded Systems Performance: Techniques and Tools*

A practical guide focused on performance tuning for embedded applications, this book covers profiling, benchmarking, and code optimization methods. It also discusses compiler optimizations and hardware acceleration options, enabling developers to identify and eliminate bottlenecks. The book is filled with real-world examples and tool recommendations.

## *5. Embedded Systems Design: An Introduction to Processes, Tools, and Techniques*

This introductory text provides a broad overview of embedded system development, emphasizing design methodologies that support high performance. Topics include system modeling, software architecture, and hardware-software integration. It is ideal for engineers seeking to understand the foundational concepts behind efficient embedded system design.

## *6. Advanced Embedded System Design: Architecting for Performance and Scalability*

Targeting experienced engineers, this book explores complex architectures that support scalable and high-performance embedded systems. It covers multi-core processing, distributed embedded systems, and advanced communication protocols. Readers will learn how to design systems that balance performance with flexibility.

## *7. Embedded Software Optimization: Techniques for High-Performance Systems*

This book concentrates on software-level optimizations tailored for embedded platforms, including memory management, concurrency, and real-time constraints. It offers concrete coding strategies and algorithmic improvements to enhance system throughput. The content is suitable for programmers aiming to push the limits of embedded software performance.

## *8. Low-Power and High-Performance Embedded Systems: Architectures and Design Techniques*

Balancing power consumption with performance, this book examines architectural trade-offs and design techniques in embedded systems. It addresses energy-efficient processors, dynamic voltage scaling, and power-aware scheduling. The book is essential for engineers designing systems where both speed and battery life are critical.

## *9. Embedded Systems: Architecture, Programming, and Design*

A comprehensive resource covering the full spectrum of embedded system development, this book integrates architectural principles with programming and design strategies for performance optimization. It includes detailed case studies, hardware-software co-design, and system-level considerations. The text serves as a valuable reference for building robust, high-performance

embedded solutions.

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**Who this book is for** This book is for software developers, IoT engineers, and anyone who wants to understand the process of developing high-performance embedded systems. You'll also find this book useful if you want to learn about the fundamentals of FPGA development and all aspects of firmware development in C and C++. Familiarity with the C language, digital circuits, and electronic soldering is necessary to get started.

**architecting high performance embedded systems: High-Performance Embedded Computing** Wayne Wolf, 2010-07-26 Over the past several years, embedded systems have emerged as an integral though unseen part of many consumer, industrial, and military devices. The explosive growth of these systems has resulted in embedded computing becoming an increasingly important discipline. The need for designers of high-performance, application-specific computing systems has never been greater, and many universities and colleges in the US and worldwide are now developing advanced courses to help prepare their students for careers in embedded

computing. High-Performance Embedded Computing: Architectures, Applications, and Methodologies is the first book designed to address the needs of advanced students and industry professionals. Focusing on the unique complexities of embedded system design, the book provides a detailed look at advanced topics in the field, including multiprocessors, VLIW and superscalar architectures, and power consumption. Fundamental challenges in embedded computing are described, together with design methodologies and models of computation. HPEC provides an in-depth and advanced treatment of all the components of embedded systems, with discussions of the current developments in the field and numerous examples of real-world applications. - Covers advanced topics in embedded computing, including multiprocessors, VLIW and superscalar architectures, and power consumption - Provides in-depth coverage of networks, reconfigurable systems, hardware-software co-design, security, and program analysis - Includes examples of many real-world embedded computing applications (cell phones, printers, digital video) and architectures (the Freescale Starcore, TI OMAP multiprocessor, the TI C5000 and C6000 series, and others)

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**architecting high performance embedded systems: Architecting and Building High-Speed SoCs** Mounir Maaref, 2022-12-09 Design a high-speed SoC while gaining a holistic view of the FPGA design flow and overcoming its challenges. Purchase of the print or kindle book includes a free eBook in the PDF format. Key Features Use development tools to implement and verify an SoC, including ARM CPUs and the FPGA logic Overcome the challenge of time to market by using FPGA SoCs and avoid the prohibitive ASIC NRE cost Understand the integration of custom logic accelerators and the SoC software and build them Book Description Modern and complex SoCs

can adapt to many demanding system requirements by combining the processing power of ARM processors and the feature-rich Xilinx FPGAs. You'll need to understand many protocols, use a variety of internal and external interfaces, pinpoint the bottlenecks, and define the architecture of an SoC in an FPGA to produce a superior solution in a timely and cost-efficient manner. This book adopts a practical approach to helping you master both the hardware and software design flows, understand key interconnects and interfaces, analyze the system performance and enhance it using the acceleration techniques, and finally build an RTOS-based software application for an advanced SoC design. You'll start with an introduction to the FPGA SoCs technology fundamentals and their associated development design tools. Gradually, the book will guide you through building the SoC hardware and software, starting from the architecture definition to testing on a demo board or a virtual platform. The level of complexity evolves as the book progresses and covers advanced applications such as communications, security, and coherent hardware acceleration. By the end of this book, you'll have learned the concepts underlying FPGA SoCs' advanced features and you'll have constructed a high-speed SoC targeting a high-end FPGA from the ground up. What you will learn

Understand SoC FPGAs' main features, advanced buses and interface protocols  
 Develop and verify an SoC hardware platform targeting an FPGA-based SoC  
 Explore and use the main tools for building the SoC hardware and software  
 Build advanced SoCs using hardware acceleration with custom IPs  
 Implement an OS-based software application targeting an FPGA-based SoC  
 Understand the hardware and software integration techniques for SoC FPGAs  
 Use tools to co-debug the SoC software and hardware  
 Gain insights into communication and DSP principles in FPGA-based SoCs

Who this book is for This book is for FPGA and ASIC hardware and firmware developers, IoT engineers, SoC architects, and anyone interested in understanding the process of developing a complex SoC, including all aspects of the hardware design and the associated firmware design. Prior knowledge of digital electronics, and some experience of coding in VHDL or Verilog and C or a similar language suitable for embedded systems will be required for using this book. A general understanding of FPGA and CPU architecture will also be helpful but not mandatory.

**architecting high performance embedded systems:** Modern Computer Architecture and Organization Jim Ledin, Dave Farley, 2022-05-04 A no-nonsense, practical guide to current and future processor and computer architectures that enables you to design computer systems and develop better software applications across a variety of domains

Key Features

Understand digital circuitry through the study of transistors, logic gates, and sequential logic  
 Learn the architecture of x86, x64, ARM, and RISC-V processors, iPhones, and high-performance gaming PCs  
 Study the design principles underlying the domains of cybersecurity, bitcoin, and self-driving cars

Book Description

Are you a software developer, systems designer, or computer architecture student looking for a methodical introduction to digital device architectures, but are overwhelmed by the complexity of modern systems? This step-by-step guide will teach you how modern computer systems work with the help of practical examples and exercises. You'll gain insights into the internal behavior of processors down to the circuit level and will understand how the hardware executes code developed in high-level languages. This book will teach you the fundamentals of computer systems including transistors, logic gates, sequential logic, and instruction pipelines. You will learn details of modern processor architectures and instruction sets including x86, x64, ARM, and RISC-V. You will see how to implement a RISC-V processor in a low-cost FPGA board and write a quantum computing program and run it on an actual quantum computer. This edition has been updated to cover the architecture and design principles underlying the important domains of cybersecurity, blockchain and bitcoin mining, and self-driving vehicles. By the end of this book, you will have a thorough understanding of modern processors and computer architecture and the future directions these technologies are likely to take. What you will learn

Understand the fundamentals of transistor technology and digital circuits  
 Explore the concepts underlying pipelining and superscalar processing  
 Implement a complete RISC-V processor in a low-cost FPGA  
 Understand the technology used to implement virtual machines  
 Learn about security-critical computing applications like financial transaction processing  
 Get up to speed with blockchain and the hardware architectures used in bitcoin

miningExplore the capabilities of self-navigating vehicle computing architecturesWrite a quantum computing program and run it on a real quantum computerWho this book is for This book is for software developers, computer engineering students, system designers, reverse engineers, and anyone looking to understand the architecture and design principles underlying modern computer systems: ranging from tiny, embedded devices to warehouse-size cloud server farms. A general understanding of computer processors is helpful but not required.

**architecting high performance embedded systems: Fundamentals of Embedded Systems: Design and Applications** Mr.S.Anbarasan, 2025-09-09 Author: Mr.S.Anbarasan, Assistant Professor, Department of Electronics and Communication Engineering, Tittagudi Sengunthar Engineering College, Tholudur, Tamil Nadu, India.

**architecting high performance embedded systems: The Insider's Guide to Arm Cortex-M Development** Zachary Lasiuk, Pareena Verma, Jason Andrews, 2022-10-27 Learn and implement the latest Arm Cortex-M microcontroller development concepts such as performance optimization, security, software reuse, machine learning, continuous integration, and cloud-based development from industry experts Key Features Learn how to select the best Cortex-M hardware, software, and tools for your project Understand the use of key software components and how to optimize and develop modern applications Get hands-on experience implementing quality software using example code provided in the book Purchase of the print or Kindle book includes a free eBook in the PDF format Book DescriptionCortex-M has been around since 2004, so why a new book now? With new microcontrollers based on the Cortex-M55 and Cortex-M85 being introduced this year, Cortex-M continues to expand. New software concepts, such as standardized software reuse, have emerged alongside new topics including security and machine learning. Development methodologies have also significantly advanced, with more embedded development taking place in the cloud and increased levels of automation. Due to these advances, a single engineer can no longer understand an entire project and requires new skills to be successful. This book provides a unique view of how to navigate and apply the latest concepts in microcontroller development. The book is split into two parts. First, you'll be guided through how to select the ideal set of hardware, software, and tools for your specific project. Next, you'll explore how to implement essential topics for modern embedded developers. Throughout the book, there are examples for you to learn by working with real Cortex-M devices with all software available on GitHub. You will gain experience with the small Cortex-M0+, the powerful Cortex-M55, and more Cortex-M processors. By the end of this book, you'll be able to practically apply modern Cortex-M software development concepts.What you will learn Familiarize yourself with heuristics to identify the right components for your Cortex-M project Boot code to efficiently start up a Cortex-M device Optimize algorithms with compilers, middleware, and other means Get to grips with machine learning frameworks and implementation techniques Understand security in the embedded space with solutions like TrustZone and TF-M Explore cloud-based development methodologies to increase efficiency Dive into continuous integration frameworks and best practices Identify future trends that could impact Cortex-M software development Who this book is for This book is for practicing engineers and students working with embedded and IoT systems who want to quickly learn how to develop quality software for Arm Cortex-M processors without reading long technical manuals. If you're looking for a book that explains C or assembly language programming for the purpose of creating a single application or mastering a type of programming such as digital signal processing algorithms, then this book is NOT for you. A basic understanding of embedded hardware and software, along with general C programming skills will assist with understanding the concepts covered in this book.

**architecting high performance embedded systems: DIY Microcontroller Projects for Hobbyists** Miguel Angel Garcia-Ruiz, Pedro Cesar Santana Mancilla, 2021-07-30 A practical guide to building PIC and STM32 microcontroller board applications with C and C++ programming Key Features Discover how to apply microcontroller boards in real life to create interesting IoT projects Create innovative solutions to help improve the lives of people affected by the COVID-19 pandemic Design, build, program, and test microcontroller-based projects with the C and C++ programming

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**architecting high performance embedded systems:** *High-Performance Embedded Computing* Marilyn Wolf, 2014-03-17 High-Performance Embedded Computing, Second Edition, combines leading-edge research with practical guidance in a variety of embedded computing topics, including real-time systems, computer architecture, and low-power design. Author Marilyn Wolf presents a comprehensive survey of the state of the art, and guides you to achieve high levels of performance from the embedded systems that bring these technologies together. The book covers CPU design, operating systems, multiprocessor programs and architectures, and much more. Embedded computing is a key component of cyber-physical systems, which combine physical devices with computational resources for control and communication. This revised edition adds new content and examples of cyber-physical systems throughout the book, including design methodologies, scheduling, and wide-area CPS to illustrate the possibilities of these new systems. - Revised and updated with coverage of recently developed consumer electronics architectures and models of computing - Includes new VLIW processors such as the TI Da Vinci, and CPU simulation - Learn model-based verification and middleware for embedded systems - Supplemental material includes lecture slides, labs, and additional resources

**architecting high performance embedded systems:** *Architecture of Computing Systems - ARCS 2020* André Brinkmann, Wolfgang Karl, Stefan Lankes, Sven Tomforde, Thilo Pionteck, Carsten Trinitis, 2020-07-09 This book constitutes the proceedings of the 33rd International Conference on Architecture of Computing Systems, ARCS 2020, held in Aachen, Germany, in May 2020.\* The 12 full papers in this volume were carefully reviewed and selected from 33 submissions. 6 workshop papers are also included. ARCS has always been a conference attracting leading-edge research outcomes in Computer Architecture and Operating Systems, including a wide spectrum of topics ranging from embedded and real-time systems all the way to large-scale and parallel systems. The selected papers focus on concepts and tools for incorporating self-adaptation and self-organization mechanisms in high-performance computing systems. This includes upcoming approaches for runtime modifications at various abstraction levels, ranging from hardware changes to goal changes and their impact on architectures, technologies, and languages. \*The conference was canceled due to the COVID-19 pandemic.

**architecting high performance embedded systems: Modeling and Optimization of Parallel and Distributed Embedded Systems** Arslan Munir, Ann Gordon-Ross, Sanjay Ranka, 2015-12-28 This book introduces the state-of-the-art in research in parallel and distributed embedded systems, which have been enabled by developments in silicon technology, micro-electro-mechanical systems (MEMS), wireless communications, computer networking, and digital electronics. These systems have diverse applications in domains including military and defense, medical, automotive, and unmanned autonomous vehicles. The emphasis of the book is on the modeling and optimization of emerging parallel and distributed embedded systems in relation to the three key design metrics of performance, power and dependability. Key features: Includes an embedded wireless sensor networks case study to help illustrate the modeling and optimization of distributed embedded systems. Provides an analysis of multi-core/many-core based embedded systems to explain the modeling and optimization of parallel embedded systems. Features an application metrics estimation model; Markov modeling for fault tolerance and analysis; and queueing theoretic modeling for performance evaluation. Discusses optimization approaches for distributed wireless sensor networks; high-performance and energy-efficient techniques at the architecture, middleware and software levels for parallel multicore-based embedded systems; and dynamic optimization methodologies. Highlights research challenges and future research directions. The book is primarily aimed at researchers in embedded systems; however, it will also serve as an invaluable reference to senior undergraduate and graduate students with an interest in embedded systems research.

**architecting high performance embedded systems: Advances in Computer Systems Architecture** Pen-Chung Yew, Jingling Xue, 2004-08-19 On behalf of the program committee, we were pleased to present this year's program for ACSAC: Asia-Pacific Computer Systems Architecture Conference. Now in its ninth year, ACSAC continues to provide an excellent forum for researchers, educators and practitioners to come to the Asia-Pacific region to exchange ideas on the latest developments in computer systems architecture. This year, the paper submission and review processes were semiautomated using the free version of CyberChair. We received 152 submissions, the largest number ever. Each paper was assigned at least three, mostly four, and in a few cases even seven committee members for review. All of the papers were reviewed in a two-month period, during which the program chairs regularly monitored the progress of the review process. When reviewers claimed inadequate expertise, additional reviewers were solicited. In the end, we received a total of 594 reviews (3.9 per paper) from committee members as well as 248 coreviewers whose names are acknowledged in the proceedings. We would like to thank all of them for their time and effort in providing us with such timely and high-quality reviews, some of them on extremely short notice.

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